

REMARKS

Claims 1-5, 7-16, 18-28 and 30-33 are pending in this application. By this Amendment, claims 1, 7, 11, 12, 18, 21, 22, 28 and 32-33 are amended and claims 6, 17, 29 and 34 are canceled without prejudice or disclaimer.

Entry of this Amendment is proper under 37 C.F.R. §1.116 because the amendments: a) place the application in condition for allowance for the reasons set forth below; b) do not raise any new issues that require further search and/or consideration; and/or c) place the application in better form for an appeal should an appeal be necessary. More specifically, the above amendments amend the independent claims to incorporate allowable features (relating to the bias circuit applying a bias voltage to the voltage controlled oscillator) previously recited in dependent claims 29 and 34. The amendment to claims 11 and 21 are merely for clarity. Thus, no new issues are raised. Entry of this Amendment is therefore proper under 37 C.F.R. §1.116.

Applicants gratefully acknowledge the Office Action's indication that claims 28-30 and 32-34 contain allowable subject matter. By this Amendment, claim 32 is rewritten into independent form. Thus, independent claim 32 defines patentable subject matter.

The Office Action rejects claims 1-7, 9-11, 22-27 and 31 under 35 U.S.C. §103(a) over U.S. Patent 6,218,876 to Sung et al. (hereafter Sung) in view of U.S. Patent 6,570,456 to Mano et al. (hereafter Mano). The Office Action rejects claim 8 under 35 U.S.C. §103(a) over Sung and Mano and further in view of U.S. Patent 6,816,019 to Delbo et al. (hereafter Delbo). The Office Action rejects claim 12 under 35 U.S.C. §102(e) by Japanese Patent 02003298417A to Sato. The Office Action rejects claims 13-18 and 20-21 under 35 U.S.C. §103(a) over Sato in view of Sung.

Reply to Office Action dated October 7, 2005

The Office Action further rejects claim 19 under 35 U.S.C. §103(a) over Sato in view of Mano, and further in view of Delbo. The rejections are respectfully traversed.

Independent claim 1 recites a second phase lock loop device, coupled to the first phase lock loop device, the second phase lock loop device including a bias circuit to be powered by the first power supply voltage and a second power supply voltage and the second phase lock loop device including a voltage controlled oscillator to be powered by the second power supply. Independent claim 1 also recites the bias circuit to provide a bias voltage to the voltage controlled oscillator, the bias voltage being different than the first power supply voltage and the second power supply voltage, the second phase lock loop device to output a clock signal having an adaptive frequency based on the second power supply voltage. Features provided within amended independent claim 1 were previously recited in dependent claim 29, which is indicated as containing allowable subject matter.

The applied references do not teach or suggest all the features of independent claim 1. The Office Action appears to rely on a combination of Sung and Mano. More specifically, the Office Action appears to suggest that Sung discloses a first phase lock loop device 52 and a second phase lock loop device 54. The Office Action agrees that Sung does not disclose first and second phase lock loop devices with first and second power supply voltages. The Office Action then appears to rely on Mano as disclosing a phase lock loop with first and second power supply voltages, allegedly Vdd and V11.

However, Mano does not teach or suggest a second phase lock loop device that includes a bias circuit to be powered by a first power supply voltage and the second power supply voltage

Reply to Office Action dated October 7, 2005

and the second phase lock loop device including a voltage controlled oscillator to be powered by the second power supply. Mano's bias circuit B21 is not powered by voltages V11 and Vdd (which the Office Action alleges corresponds to the claimed first power supply voltage and second power supply voltage). For at least these reasons, Mano and Sung do not teach or suggest all of the features of independent claim 1. Thus, independent claim 1 defines patentable subject matter.

Independent claim 12 defines patentable subject matter for at least similar reasons. That is, independent claim 12 recites an adaptive phase lock loop device including a bias circuit to be powered by the analog power supply voltage and the digital power supply voltage, and the adaptive phase lock loop device including a voltage controlled oscillator (VCO) to be powered by the digital power supply voltage. Independent claim 12 also recites the bias circuit to provide a bias voltage to the voltage controlled oscillator, the bias voltage being different than the analog power supply voltage and the digital power supply voltage. Sato does not teach or suggest these features. Additionally, Sung and Mano do not teach or suggest these features for at least similar reasons as set forth above, Thus, independent claim 12 defines patentable subject matter.

Additionally, independent claim 22 defines patentable subject matter for at least similar reasons. That is, independent claim 22 recites a second phase lock loop device including a bias circuit to be powered by the first power supply voltage and the second power supply voltage, the second phase lock loop device also including a voltage controlled oscillator to be powered by the second power supply, the bias circuit to provide a bias voltage to the voltage controlled oscillator, the bias voltage being different than the first power supply voltage and the second

Reply to Office Action dated October 7, 2005

power supply voltage. For at least similar reasons as set forth above, the applied references do not teach or suggest these features. Thus, independent claim 22 defines patentable subject matter.

Accordingly, each of independent claims 1, 12, 22 and 32 define patentable subject matter. Each of the dependent claims depends from one of the independent claims and therefore defines patentable subject matter at least for this reason. In addition, the dependent claims recite features that further and independently distinguish over the applied references.

CONCLUSION

In view of the foregoing, it is respectfully submitted that the application is in condition for allowance. Favorable consideration and prompt allowance of claims 1-5, 7-16, 18-28 and 30-33 are earnestly solicited. If the Examiner believes that any additional changes would place the application in better condition for allowance, the Examiner is invited to contact the undersigned attorney at the telephone number listed below.

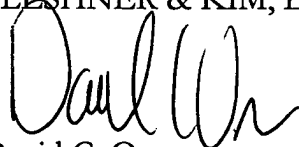
Serial No. **10/813,551**

Docket No. **INTEL-0054**

Reply to Office Action dated October 7, 2005

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this, concurrent and future replies, including extension of time fees, to Deposit Account 16-0607 and please credit any excess fees to such deposit account.

Respectfully submitted,
FLESHNER & KIM, LLP



David C. Oren
Registration No. 38,694
Attorney for Intel Corporation

P.O. Box 221200
Chantilly, Virginia 20153-1200
703 766-3701 DCO/kah

Date: January 9, 2006

Please direct all correspondence to Customer Number 34610